



FUKUCOM COMPANY LTD.

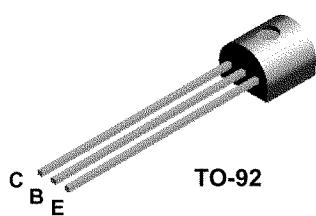
福靈有限公司

FLAT P, 3/F, EVEREST INDUSTRIAL CENTRE, 396 KWUN TONG ROAD,
KWUN TONG, KOWLOON, HONG KONG.
TEL: 2790-0314 FAX: 2790-0206

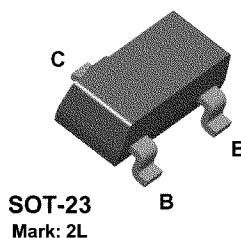
2N5401 / MMBT5401

FAIRCHILD
SEMICONDUCTOR™

2N5401



MMBT5401



PNP General Purpose Amplifier

This device is designed as a general purpose amplifier and switch for applications requiring high voltages.

Absolute Maximum Ratings*

TA = 25°C unless otherwise noted

Symbol	Parameter	Value	Units
V_{CEO}	Collector-Emitter Voltage	150	V
V_{CBO}	Collector-Base Voltage	160	V
V_{EBO}	Emitter-Base Voltage	5.0	V
I_C	Collector Current - Continuous	600	mA
T_J, T_{stg}	Operating and Storage Junction Temperature Range	-55 to +150	°C

* These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

NOTES:

- 1) These ratings are based on a maximum junction temperature of 150 degrees C.
- 2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.
- 3) All voltages (V) and currents (A) are negative polarity for PNP transistors.

Thermal Characteristics

TA = 25°C unless otherwise noted

Symbol	Characteristic	Max		Units
		2N5401	*MMBT5401	
P_D	Total Device Dissipation Derate above 25°C	625 5.0	350 2.8	mW mW/°C
$R_{\theta JC}$	Thermal Resistance, Junction to Case	83.3		°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	200	357	°C/W

* Device mounted on FR-4 PCB 1.6" X 1.6" X 0.06."



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(continued)

Electrical Characteristics

TA = 25°C unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Max	Units
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OFF CHARACTERISTICS

V _{(BR)CEO}	Collector-Emitter Breakdown Voltage*	I _C = 1.0 mA, I _B = 0	150		V
V _{(BR)CBO}	Collector-Base Breakdown Voltage	I _C = 100 µA, I _E = 0	160		V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	I _E = 10 µA, I _C = 0	5.0		V
I _{CBO}	Collector Cutoff Current	V _{CB} = 120 V, I _E = 0 V _{CB} = 120 V, I _E = 0, TA = 100°C		50	nA µA
I _{EBO}	Emitter Cutoff Current	V _{EB} = 3.0 V, I _C = 0		50	nA

ON CHARACTERISTICS*

h _{FE}	DC Current Gain	I _C = 1.0 mA, V _{CE} = 5.0 V I _C = 10 mA, V _{CE} = 5.0 V I _C = 50 mA, V _{CE} = 5.0 V	50 60 50	240	
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 10 mA, I _B = 1.0 mA I _C = 50 mA, I _B = 5.0 mA		0.2 0.5	V V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = 10 mA, I _B = 1.0 mA I _C = 50 mA, I _B = 5.0 mA		1.0 1.0	V V

SMALL SIGNAL CHARACTERISTICS

f _T	Current Gain - Bandwidth Product	I _C = 10 mA, V _{CE} = 10 V, f = 100 MHz	100	300	MHz
C _{obo}	Output Capacitance	V _{CB} = 10 V, I _E = 0, f = 1.0 MHz		6.0	pF
NF	Noise Figure	I _C = 250 µA, V _{CE} = 5.0 V, R _S = 1.0 kΩ, f = 10 Hz to 15.7 kHz		8.0	dB

*Pulse Test: Pulse Width ≤ 300 µs, Duty Cycle ≤ 2.0%

NOTE: All voltages (V) and currents (A) are negative polarity for PNP transistors.

Spice Model

PNP (Is=21.48f Xti=3 Eg=1.11 Vaf=100 Bf=132.1 Ne=1.375 Ise=21.48f Ikf=.1848 Xtb=1.5 Br=3.661 Nc=2 Isc=0
Ikrf=0 Rc=1.6 Cjc=17.63p Mjc=.5312 Vjc=.75 Fc=.5 Cje=73.39p Mje=.3777 Vje=.75 Tr=1.476n Tf=641.9p Itf=0
Vtf=0 Xtf=0 Rb=10)



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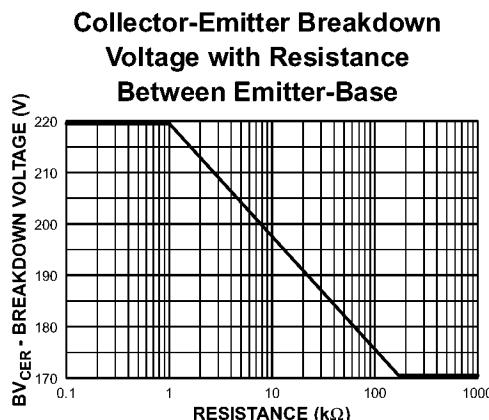
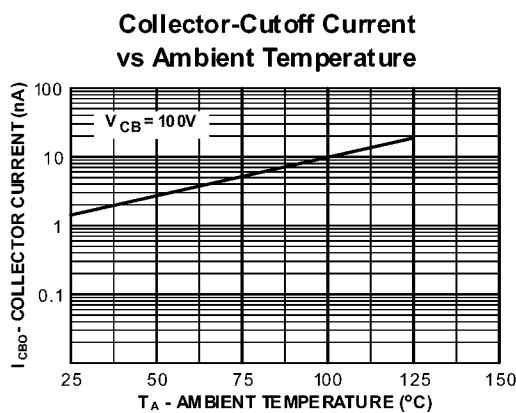
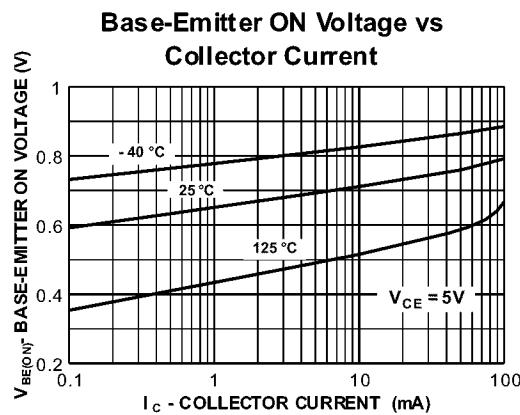
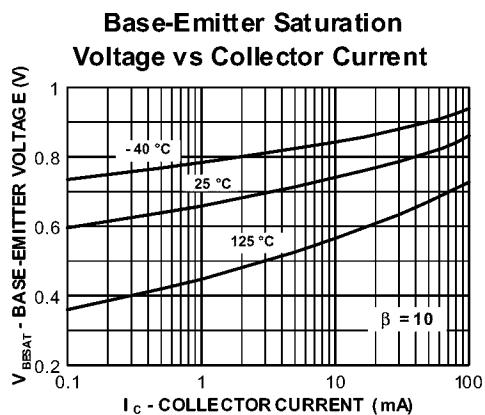
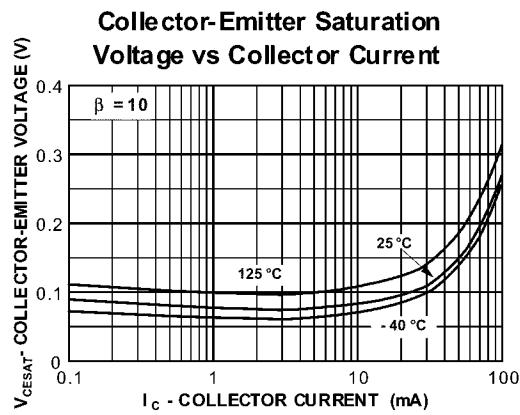
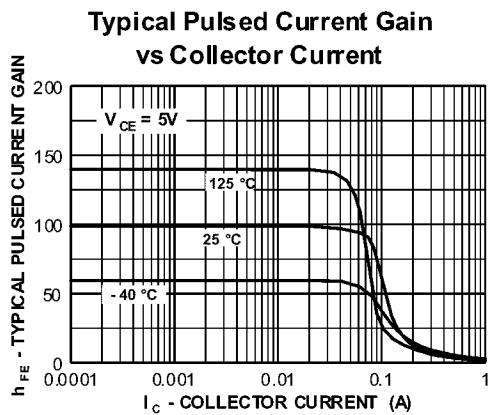
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PNP General Purpose Amplifier

(continued)

Typical Characteristics





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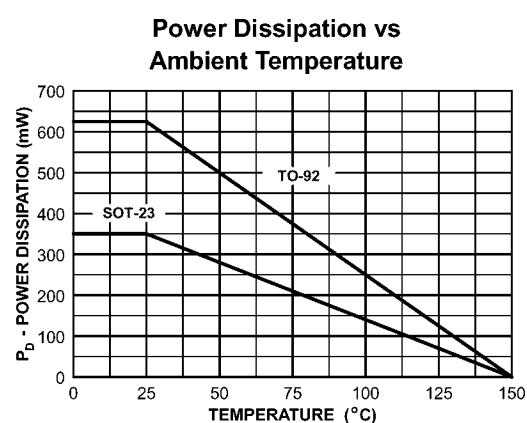
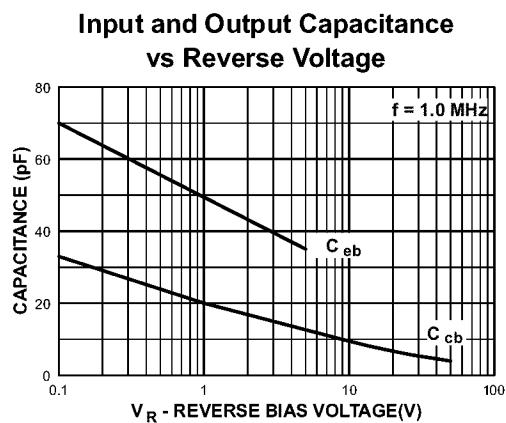
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PNP General Purpose Amplifier
(continued)

Typical Characteristics (continued)





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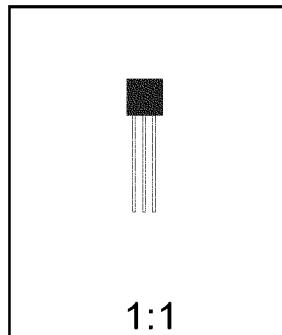
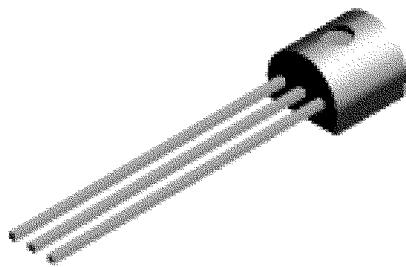
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TO-92 Package Dimensions

FAIRCHILD
SEMICONDUCTOR™

TO-92 (FS PKG Code 92, 94, 96)



1:1

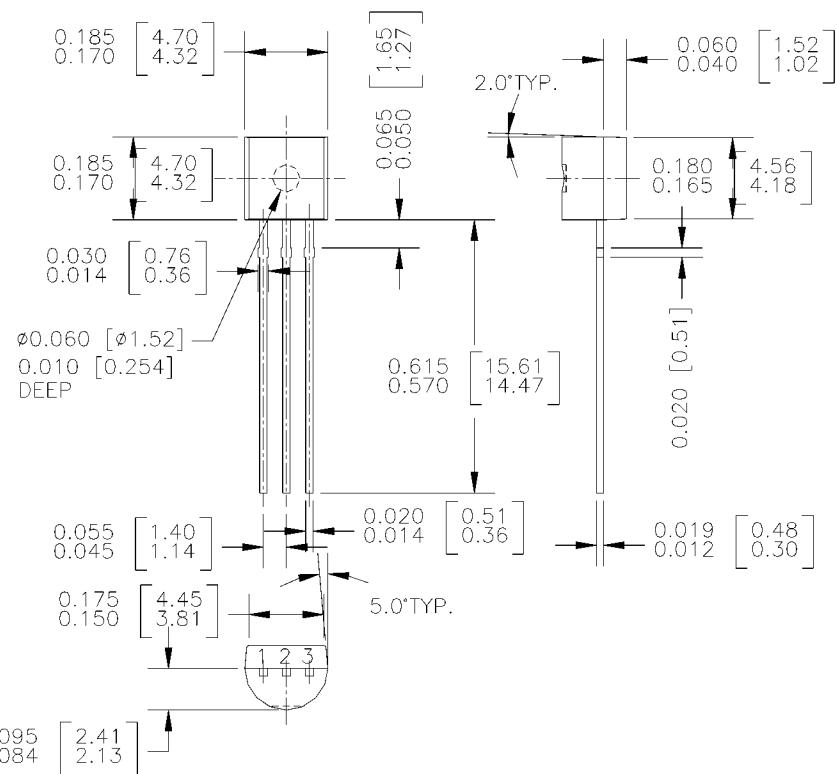
Scale 1:1 on letter size paper

Dimensions shown below are in:
inches [millimeters]

Part Weight per unit (gram): 0.1977

TO-92 (92,94,96)

PIN	92		94		96	
	B	F	B	F	B	F
1	E	D	E	D	B	S
2	B	S	C	G	E	D
3	C	G	B	S	C	G





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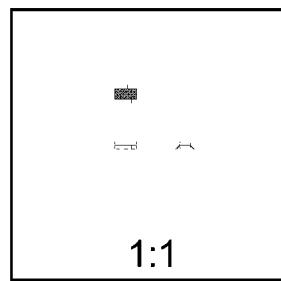
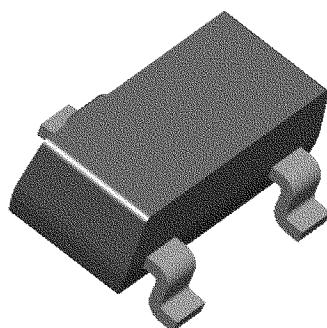
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SOT-23 Package Dimensions

FAIRCHILD
SEMICONDUCTOR™

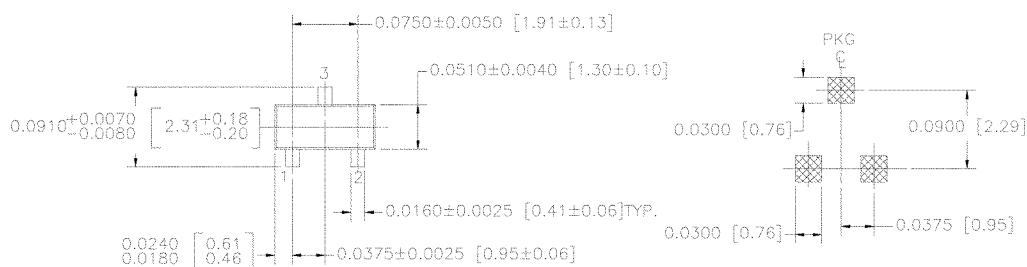
SOT-23 (FS PKG Code 49)



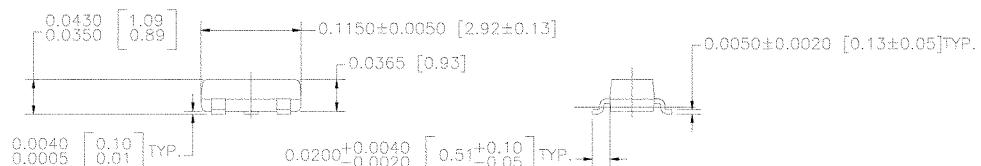
Scale 1:1 on letter size paper

Dimensions shown below are in:
inches [millimeters]

Part Weight per unit (gram): 0.0082



LAND PATTERN RECOMMENDATION



CONTROLLING DIMENSION IS INCH
VALUES IN [] ARE MILLIMETERS

SOT 23, 3 LEADS LOW PROFILE

NOTE : UNLESS OTHERWISE SPECIFIED

1. STANDARD LEAD FINISH 150 MICROINCHES / 3.81 MICROMETERS
MINIMUM TIN / LEAD (SOLDER) ON ALLOY 42

2. REFERENCE JEDEC REGISTRATION TO-236, VARIATION AB, ISSUE G, DATED JUL 1993