

福靈有限公司

FLAT P, 3/F., EVEREST INDUSTRIAL CENTRE, 396 KWUN TONG ROAD, KWUN TONG, KOWLOON, HONG KONG.

TEL: 852-2790 0314 FAX: 852-2790 0206

MC7800, MC7800A, NCV7805

1.0 A Positive Voltage Regulators

These voltage regulators are monolithic integrated circuits designed as fixed—voltage regulators for a wide variety of applications including local, on—card regulation. These regulators employ internal current limiting, thermal shutdown, and safe—area compensation. With adequate heatsinking they can deliver output currents in excess of 1.0 A. Although designed primarily as a fixed voltage regulator, these devices can be used with external components to obtain adjustable voltages and currents.

- Output Current in Excess of 1.0 A
- No External Components Required
- · Internal Thermal Overload Protection
- Internal Short Circuit Current Limiting
- · Output Transistor Safe-Area Compensation
- Output Voltage Offered in 2% and 4% Tolerance
- Available in Surface Mount D²PAK-3, DPAK-3 and Standard 3-Lead Transistor Packages
- NCV Prefix for Automotive and Other Applications Requiring Site and Control Changes
- Pb-Free Packages are Available

MAXIMUM RATINGS (T_A = 25°C, unless otherwise noted)

		Value		Unit	
Rating	Symbol	369C	221A	936	
Input Voltage (5.0 - 18 V) (24 V)	VI	35 40			Vdc
Power Dissipation	P _D	Internally Limited			W
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	92	65	Figure 14	°C/W
Thermal Resistance, Junction-to-Case	R _{θJC}	5.0	5.0	5.0	°C/W
Storage Junction Temperature Range	T _{stg}	-65 to +150		°C	
Operating Junction Temperature	Tj	+150			°C

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

NOTE: ESD data available upon request.



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TO-220-3 T SUFFIX CASE 221A

Heatsink surface connected to Pin 2.



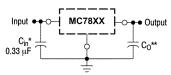
Pin 1. Input 2. Ground 3. Output D²PAK-3 D2T SUFFIX CASE 936

Heatsink surface (shown as terminal 4 in case outline drawing) is connected to Pin 2.



DPAK-3 DT SUFFIX CASE 369C

STANDARD APPLICATION



A common ground is required between the input and the output voltages. The input voltage must remain typically 2.0 V above the output voltage even during the low point on the input ripple voltage.

- XX, These two digits of the type number indicate nominal voltage.
 - C_{in} is required if regulator is located an appreciable distance from power supply filter.
- ** C_O is not needed for stability; however, it does improve transient response. Values of less than 0.1 μF could cause instability.

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 21 of this data sheet.

DEVICE MARKING INFORMATION

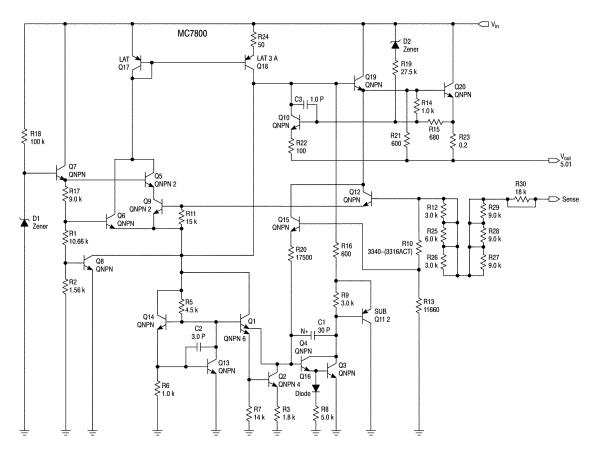
See general marking information in the device marking section on page 25 of this data sheet.



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This device contains 22 active transistors.

Figure 1. Representative Schematic Diagram



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 $\textbf{ELECTRICAL CHARACTERISTICS} \ (V_{in} = 15 \ V, \ I_{O} = 500 \ \text{mA}, \ T_{J} = T_{low} \ \text{to} \ T_{high} \ (\text{Note 13}), \ unless \ otherwise \ noted)$

			MC7809B			MC7809C		
Characteristic	Symbol	Min	Тур	Max	Min	Тур	Max	Unit
Output Voltage (T _J = 25°C)	Vo	8.65	9.0	9.35	8.65	9.0	9.35	Vdc
Output Voltage (5.0 mA \leq I _O \leq 1.0 A, P _D \leq 15 W)	Vo							Vdc
11.5 Vdc ≤ V _{in} ≤ 24 Vdc		8.55	9.0	9.45	8.55	9.0	9.45	
Line Regulation, T _J = 25°C (Note 14)	Reg _{line}							mV
11 Vdc ≤ V _{in} ≤ 26 Vdc		-	6.2	32	-	6.2	32	
11.5 Vdc ≤ V _{in} ≤ 17 Vdc		-	1.8	16	-	1.8	16	
Load Regulation, T _J = 25°C (Note 14)	Reg _{load}	-	1.5	35	-	1.5	35	mV
5.0 mA ≤ I _O ≤ 1.5 A								
Quiescent Current	I _B	-	3.4	8.0	-	3.4	8.0	mA
Quiescent Current Change	Δl_{B}							mA
11.5 Vdc ≤ V _{in} ≤ 26 Vdc		-	_	1.0	_	-	1.0	
5.0 mA ≤ I _O ≤ 1.0 A		-	-	0.5	_	-	0.5	
Ripple Rejection	RR	56	61	-	56	61	-	dB
11.5 Vdc ≤ V _{in} ≤ 21.5 Vdc, f = 120 Hz								
Dropout Voltage (I _O = 1.0 A, T _J = 25°C)	V _I - V _O	-	2.0	-	_	2.0	-	Vdc
Output Noise Voltage (T _A = 25°C)	V _n	-	10	-	-	10	_	μV/V _O
10 Hz ≤ f ≤ 100 kHz								
Output Resistance f = 1.0 kHz	r _O	-	1.0	-	-	1.0	-	mΩ
Short Circuit Current Limit (T _A = 25°C)	I _{SC}	-	0.2	-	-	0.2	-	Α
V _{in} = 35 Vdc								
Peak Output Current (T _J = 25°C)	I _{max}	-	2.2	-	-	2.2	-	А
Average Temperature Coefficient of Output Voltage	TCV _O	-	-0.5	-	-	-0.5	-	mV/°C

 $[\]begin{array}{ll} 13.\,T_{low} = 0^{\circ}\textrm{C for MC78XXAC, C} & T_{high} = +125^{\circ}\textrm{C for MC78XXAC, C, NCV7805} \\ = -40^{\circ}\textrm{C for MC78XXB, MC78XXAB, NCV7805} \end{array}$

^{14.} Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used.

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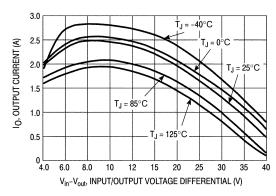


Figure 2. Peak Output Current as a Function of Input/Output Differential Voltage (MC78XXC, AC, B)

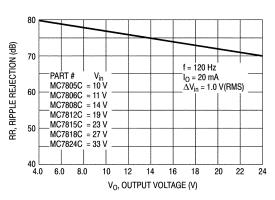


Figure 3. Ripple Rejection as a Function of Output Voltages (MC78XXC, AC, B)

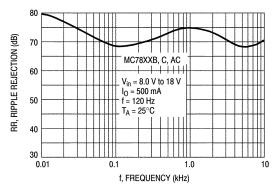


Figure 4. Ripple Rejection as a Function of Frequency (MC78XXC, AC, B)

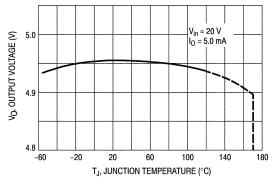


Figure 5. Output Voltage as a Function of Junction Temperature (MC7805C, AC, B)

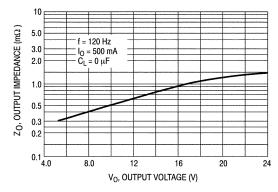


Figure 6. Output Impedance as a Function of Output Voltage (MC78XXC, AC, B)

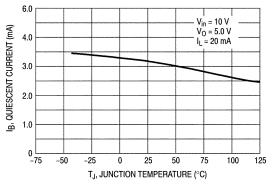


Figure 7. Quiescent Current as a Function of Temperature (MC78XXC, AC, B)

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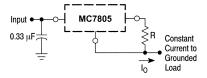
APPLICATIONS INFORMATION

Design Considerations

The MC7800 Series of fixed voltage regulators are designed with Thermal Overload Protection that shuts down the circuit when subjected to an excessive power overload condition, Internal Short Circuit Protection that limits the maximum current the circuit will pass, and Output Transistor Safe—Area Compensation that reduces the output short circuit current as the voltage across the pass transistor is increased.

In many low current applications, compensation capacitors are not required. However, it is recommended that the regulator input be bypassed with a capacitor if the regulator is connected to the power supply filter with long

wire lengths, or if the output load capacitance is large. An input bypass capacitor should be selected to provide good high–frequency characteristics to insure stable operation under all load conditions. A 0.33 μF or larger tantalum, mylar, or other capacitor having low internal impedance at high frequencies should be chosen. The bypass capacitor should be mounted with the shortest possible leads directly across the regulators input terminals. Normally good construction techniques should be used to minimize ground loops and lead resistance drops since the regulator has no external sense lead.



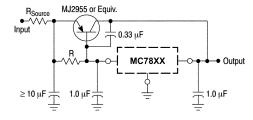
The MC7800 regulators can also be used as a current source when connected as above. In order to minimize dissipation the MC7805C is chosen in this application. Resistor R determines the current as follows:

$$I_0 = \frac{5.0 \, V}{R} + I_B$$

 $I_B \cong 3.2$ mA over line and load changes.

For example, a 1.0 A current source would require R to be a 5.0 Ω , 10 W resistor and the output voltage compliance would be the input voltage less 7.0 V.

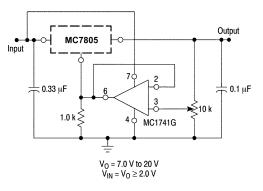
Figure 8. Current Regulator



XX = 2 digits of type number indicating voltage.

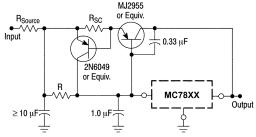
The MC7800 series can be current boosted with a PNP transistor. The MJ2955 provides current to 5.0 A. Resistor R in conjunction with the V_{BE} of the PNP determines when the pass transistor begins conducting; this circuit is not short circuit proof. Input/output differential voltage minimum is increased by V_{BE} of the pass transistor.

Figure 10. Current Boost Regulator



The addition of an operational amplifier allows adjustment to higher or intermediate values while retaining regulation characteristics. The minimum voltage obtainable with this arrangement is 2.0 V greater than the regulator voltage.

Figure 9. Adjustable Output Regulator



XX = 2 digits of type number indicating voltage.

The circuit of Figure 10 can be modified to provide supply protection against short circuits by adding a short circuit sense resistor, $R_{\rm SC}$, and an additional PNP transistor. The current sensing PNP must be able to handle the short circuit current of the three-terminal regulator. Therefore, a four-ampere plastic power transistor is specified.

Figure 11. Short Circuit Protection

3m

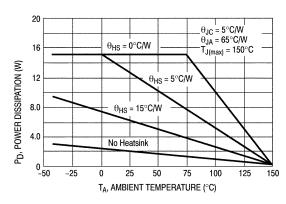
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 $I_0 = 1.0 A$ V_{in} - V_{out}, INPUT-OUTPUT VOLTAGE I_O = 500 mA 2.0 $I_0 = 200 \text{ mA}$ DIFFERENTIAL (V) $I_0 = 20 \text{ mA}$ $I_0 = 0 \text{ mA}$ 0.5 $\Delta V_0 = 2\%$ of V_0 - Extended Curve for MC78XXB 0 <u>└</u> -75 -50 -25 0 25 50 75 100 125 T_J, JUNCTION TEMPERATURE (°C)

Figure 12. Worst Case Power Dissipation versus Ambient Temperature (Case 221A)

Figure 13. Input Output Differential as a Function of Junction Temperature (MC78XXC, AC, B)

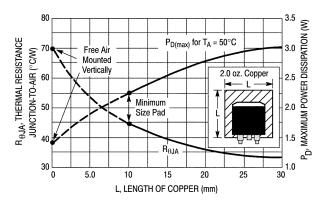


Figure 14. D²PAK Thermal Resistance and Maximum Power Dissipation versus P.C.B. Copper Length

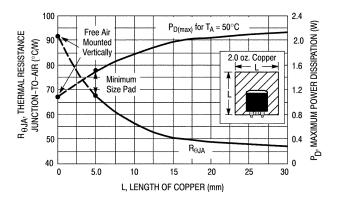


Figure 15. DPAK Thermal Resistance and Maximum Power Dissipation versus P.C.B. Copper Length



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ORDERING INFORMATION

Device	Output Voltage	Temperature Range	Package	Shipping [†]
MC7808CD2T			D2PAK	50 Units / Rail
MC7808CD2TR4			D2PAK	800 / Tape & Reel
MC7808CD2TR4G			D2PAK (Pb-Free)	800 / Tape & Reel
MC7808CDT	1		DPAK	75 Units / Rail
MC7808CDTRK	8.0 V	T = 0° to +125°C	DPAK	2500 / Tape & Reel
MC7808CDTT5	7	1 - 0 10 1 123 0	DPAK	2500 / Tape & Reel
MC7808CDTT5G			DPAK (Pb-Free)	2500 / Tape & Reel
MC7808CT			TO-220	50 Units / Rail
MC7808CTG			TO-220 (Pb-Free)	50 Units / Rail
MC7809ACT		T = 0° to +125°C	TO-220	50 Units / Rail
MC7809BT		T = -40° to +125°C	TO-220	50 Units / Rail
MC7809CD2T			D2PAK	50 Units / Rail
MC7809CD2TR4	9.0 V		D2PAK	800 / Tape & Reel
MC7809CT		T = 0° to +125°C	TO-220	50 Units / Rail
MC7809CTG			TO-220 (Pb-Free)	50 Units / Rail
MC7812ABD2T		T = -40° to +125°C	D2PAK	50 Units / Rail
MC7812ABD2TR4			D2PAK	800 / Tape & Reel
MC7812ABT			TO-220	50 Units / Rail
MC7812ACD2T			D2PAK	50 Units / Rail
MC7812ACD2TR4	7	T = 0° to +125°C	D2PAK	800 / Tape & Reel
MC7812ACT			TO-220	50 Units / Rail
MC7812ACTG			TO-220 (Pb-Free)	50 Units / Rail
MC7812BD2T			D2PAK	50 Units / Rail
MC7812BD2TR4] 12 V		D2PAK	800 / Tape & Reel
MC7812BD2TR4G			D2PAK (Pb-Free)	800 / Tape & Reel
MC7812BDT			DPAK	75 Units / Rail
MC7812BDTRK		T = 400 to 140500	DPAK	2500 / Tape & Reel
MC7812BT		T = -40° to +125°C	TO-220	50 Units / Rail
MC7812BTG			TO-220 (Pb-Free)	50 Units / Rail
NCV7812BD2T*			D2PAK	50 Units / Rail
NCV7812BD2TR4*			D2PAK	800 / Tape & Reel
NCV7812BT*			TO-220	50 Units / Rail

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.
*NCV devices: T_{low} = -40°C, T_{high} = +125°C. Guaranteed by design. NCV prefix is for automotive and other applications requiring site

and change control.



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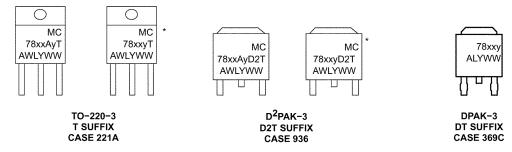
MC7800, MC7800A, NCV7805

ORDERING INFORMATION

Device	Output Voltage	Temperature Range	Package	Shipping [†]
MC7818ACT		T = 0° to +125°C	TO-220	50 Units / Rail
MC7818BT		T = -40° to +125°C	TO-220	50 Units / Rail
MC7818CD2T			D2PAK	50 Units / Rail
MC7818CD2TR4	18 V		D2PAK	800 / Tape & Reel
MC7818CT		T = 0° to +125°C	TO-220	50 Units / Rail
MC7818CTG			TO-220 (Pb-Free)	50 Units / Rail
MC7824ACT			TO-220	50 Units / Rail
MC7824ACTG		T = 0° to +125°C	TO-220 (Pb-Free)	50 Units / Rail
MC7824BD2T			D2PAK	50 Units / Rail
MC7824BD2TR4			D2PAK	800 / Tape & Reel
MC7824BT		$T = -40^{\circ} \text{ to } +125^{\circ}\text{C}$	TO-220	50 Units / Rail
MC7824BTG	24 V		TO-220 (Pb-Free)	50 Units / Rail
MC7824CD2T			D2PAK	50 Units / Rail
MC7824CD2TR4			D2PAK	800 / Tape & Reel
MC7824CT		T = 0° to +125°C	TO-220	50 Units / Rail
MC7824CTG			TO-220 (Pb-Free)	50 Units / Rail

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

MARKING DIAGRAMS



*This marking diagram also applies to NCV78xx family.

xx = 05, 06, 08, 09, 12, 15, 18, or 24

y = B or C

A = Assembly Location

WL, L = Wafer Lot Y = Year WW = Work Week

^{*}NCV devices: T_{low} = -40°C, T_{high} = +125°C. Guaranteed by design. NCV prefix is for automotive and other applications requiring site and change control.

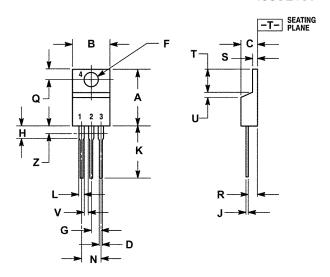


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PACKAGE DIMENSIONS

TO-220-3 **T SUFFIX** CASE 221A-09 **ISSUE AA**



- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
 3. DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED.

	INCHES		MILLIMETER	
DIM	MIN	MAX	MIN	MAX
Α	0.570	0.620	14.48	15.75
В	0.380	0.405	9.66	10.28
С	0.160	0.190	4.07	4.82
D	0.025	0.035	0.64	0.88
F	0.142	0.147	3.61	3.73
G	0.095	0.105	2.42	2.66
Н	0.110	0.155	2.80	3.93
J	0.018	0.025	0.46	0.64
K	0.500	0.562	12.70	14.27
L	0.045	0.060	1.15	1.52
N	0.190	0.210	4.83	5.33
Q	0.100	0.120	2.54	3.04
R	0.080	0.110	2.04	2.79
S	0.045	0.055	1.15	1.39
T	0.235	0.255	5.97	6.47
C	0.000	0.050	0.00	1.27
٧	0.045		1.15	
Z		0.080		2.04



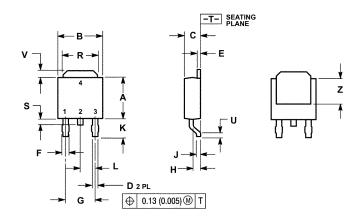
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PACKAGE DIMENSIONS

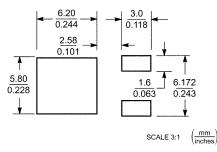
DPAK-3 DT SUFFIX CASE 369C-01 ISSUE O



- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH.

	INCHES		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.235	0.245	5.97	6.22
В	0.250	0.265	6.35	6.73
С	0.086	0.094	2.19	2.38
D	0.027	0.035	0.69	0.88
Е	0.018	0.023	0.46	0.58
F	0.037	0.045	0.94	1.14
G	0.180	BSC	4.58 BSC	
Н	0.034	0.040	0.87	1.01
J	0.018	0.023	0.46	0.58
K	0.102	0.114	2.60	2.89
L	0.090 BSC		2.29	BSC
R	0.180	0.215	4.57	5.45
S	0.025	0.040	0.63	1.01
U	0.020		0.51	
٧	0.035	0.050	0.89	1.27
Z	0.155		3.93	

SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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